

Practitioner's Docket No. NVIDP033A/P000873

PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of: James M. Van Dyke et al.

Application No.: 10/657,957

Group No.: 2186

Filed: 09/08/2003

Examiner: Woo H. Choi

For: SYSTEM AND METHOD FOR A HIGH BANDWIDTH-LOW LATENCY MEMORY
CONTROLLER

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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Terminal Disclaimer to Obviate a Double Patenting Rejection--page 1 of 2

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